

UNIT - IV

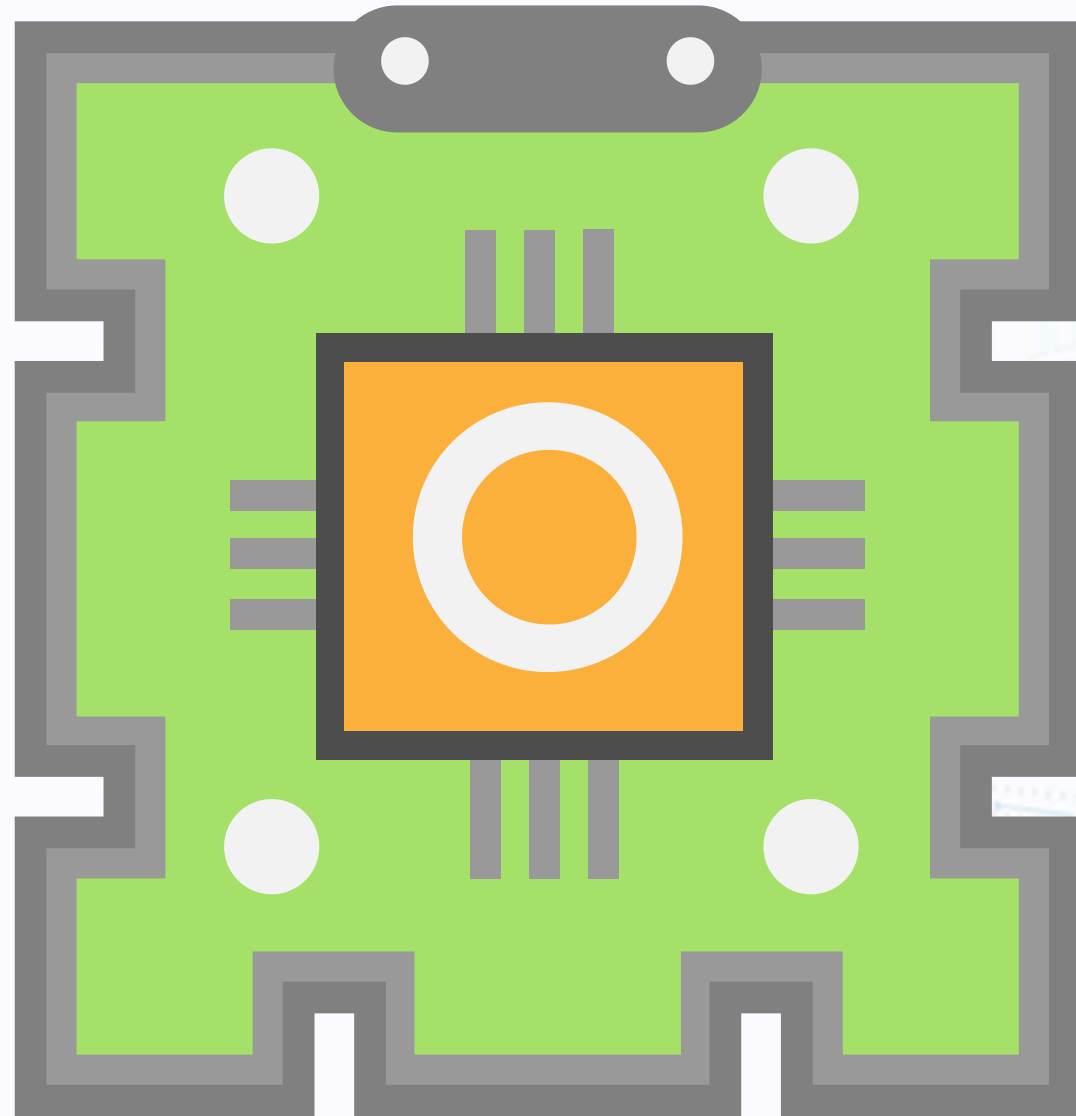
Agenda

1. What is Polygon
2. power plane and ground plane
3. Purpose of Polygon
4. Advantages of Polygon
5. Introduction to Thermal Management
6. Thermal Management Techniques

Polygon in PCB Design

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What is Polygon PCBs?



A polygon in PCB (Printed Circuit Board) design is a closed shape (usually a filled area) that is used to create large conductive regions or copper planes on a PCB. These polygons are typically used for power distribution, ground planes, or shielding.

PURPOSE of POLYGONS in PCB

GROUND AND POWER PLANES:

- Provides a large, low-impedance path for power or ground connections.
- Improves circuit stability and reduces noise.

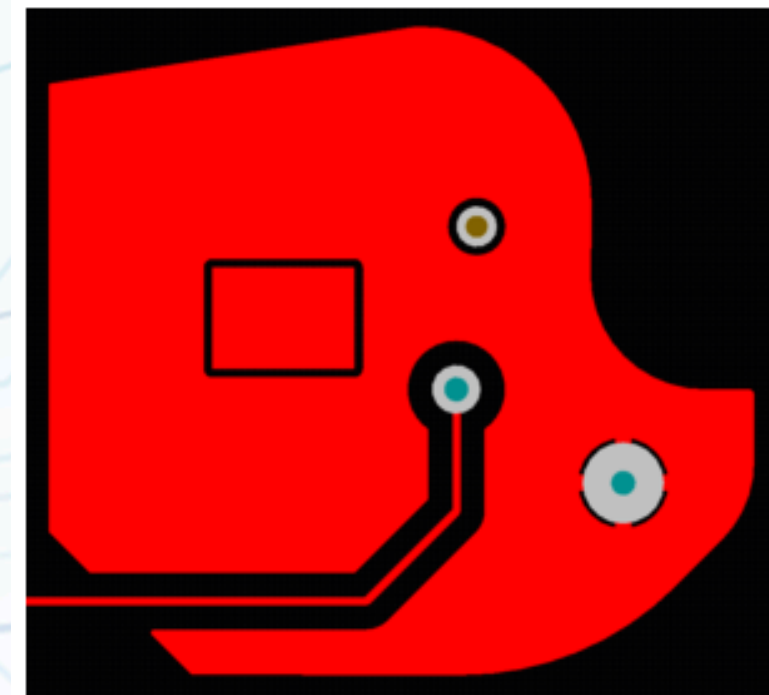
THERMAL MANAGEMENT:

- Helps in dissipating heat from high-power components by spreading it over a large area.

Types of Polygon

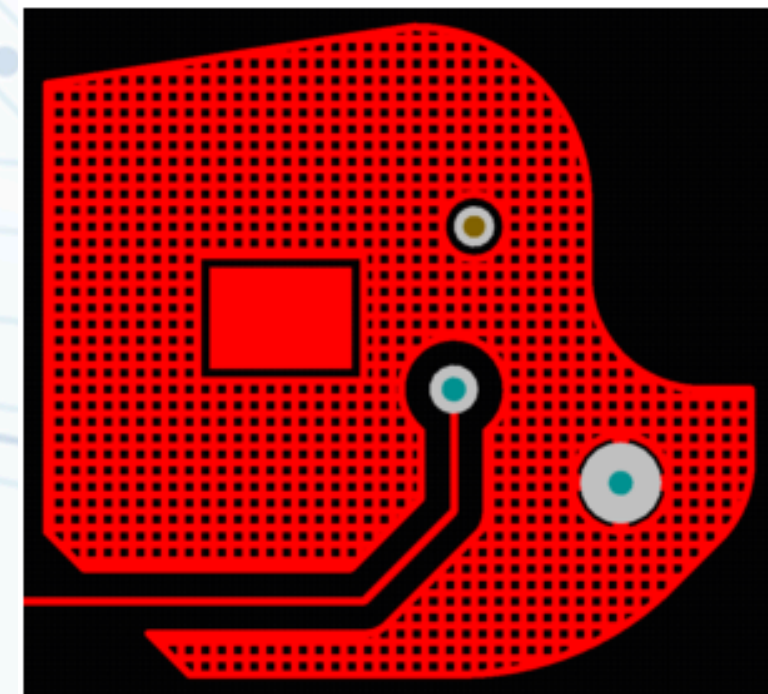
1. Solid Fill Polygons:

- Fully filled copper areas.
- Commonly used for ground or power planes.



2. Hatched Polygons:

- Filled with a mesh pattern instead of solid copper.
- Used to reduce copper usage and manage impedance.



Advantages of Using Polygons

IMPROVED ELECTRICAL PERFORMANCE

Reduces voltage drop and noise in power distribution.

BETTER THERMAL CONDUCTIVITY

Helps dissipate heat from high-power components.

EFFICIENT PCB LAYOUT

Simplifies connections and reduces routing complexity.

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INTRODUCTION TO THERMAL MANAGEMENT

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WHAT IS THERMAL MANAGEMENT IN PCBs?

Thermal management in PCBs refers to the design strategies and techniques used to control heat generation, dissipation, and distribution to ensure reliable operation and longevity of electronic components.



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Why is THERMAL MANAGEMENT Important?

HEAT GENERATION in ELECTRONICS

High-performance electronic components like processors, power regulators, and LEDs generate significant heat during operation.

IMPACT of HEAT ON PCBs

- Excessive heat can cause component failure, signal degradation, and mechanical stress.
- Uneven heat distribution may lead to thermal expansion and reliability issues.

SOURCES OF HEAT in PCBs

ACTIVE COMPONENTS

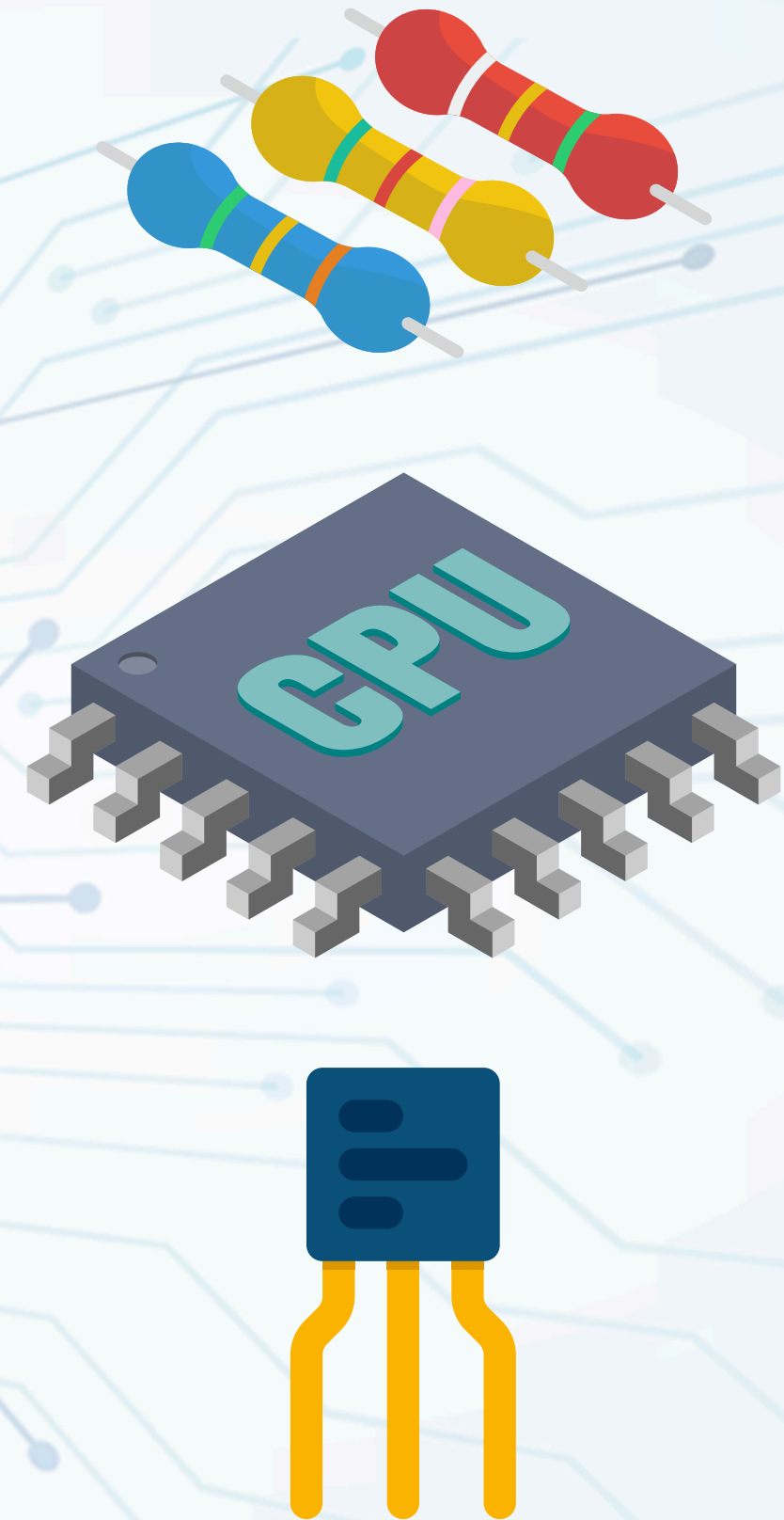
- ICs, power transistors, and LEDs.

PASSIVE COMPONENTS

- Resistors, inductors, and transformers.

ENVIRONMENTAL FACTORS

- Ambient temperature and enclosure constraints.



Key Goals of Thermal Management

Efficient Heat Dissipation:

- Transfer heat away from components to avoid overheating.

Uniform Temperature Distribution:

- Minimize hotspots on the PCB.

Optimized Design:

- Integrate thermal considerations early in the PCB design process.

Effective Thermal Management Techniques

Use of thermal vias.

Copper pours and thicker copper layers

Heat sinks and thermal interface materials.

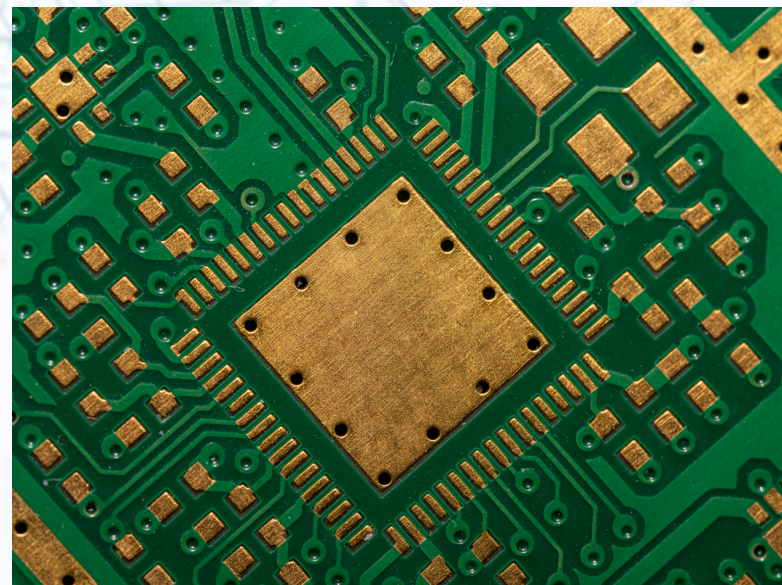
Component placement and thermal materials.

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1. Use of Thermal Vias

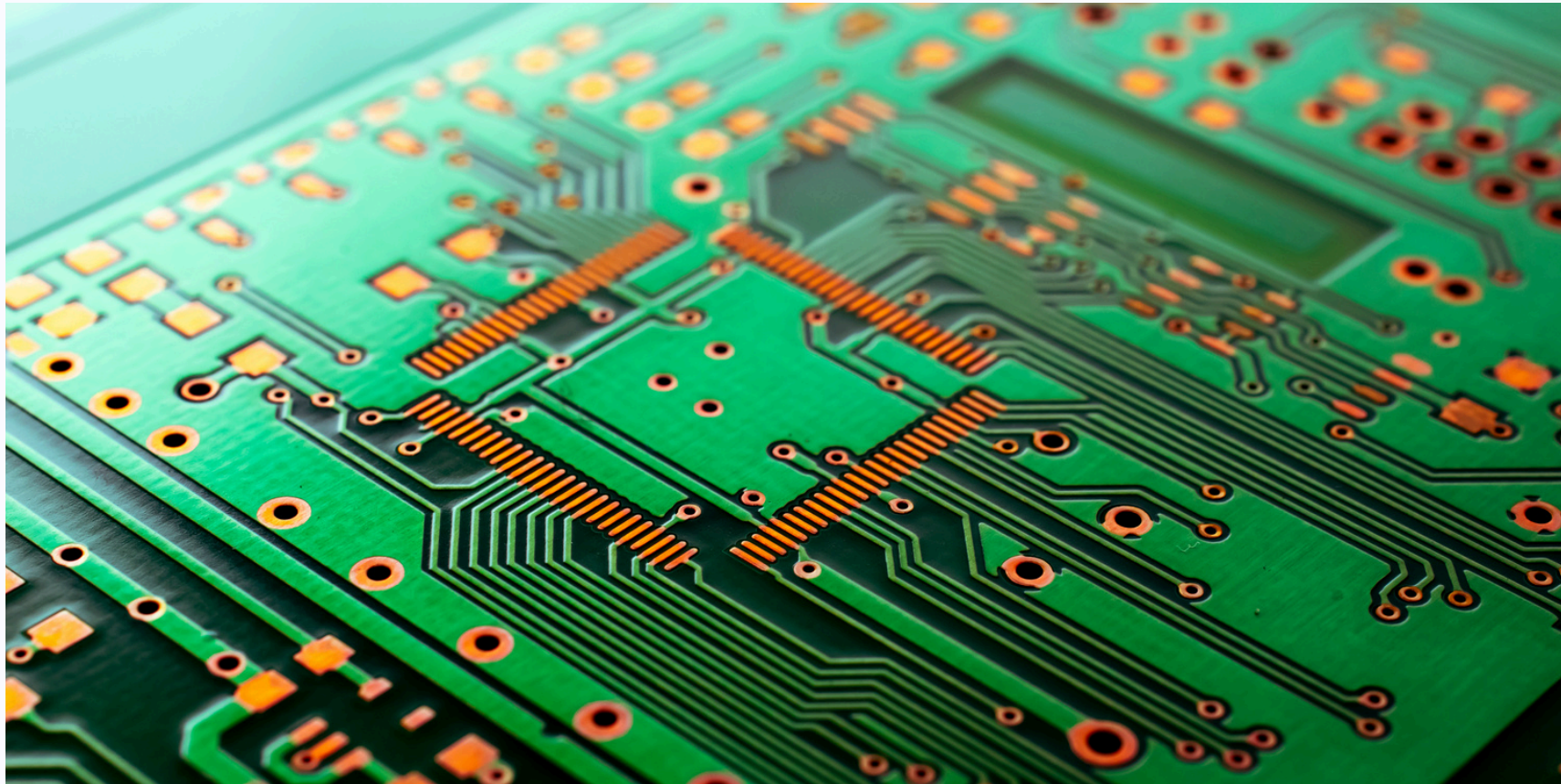
Transfers heat
between PCB
layers.

Best for high-
power
components.



Use an array for
efficient heat
transfer.

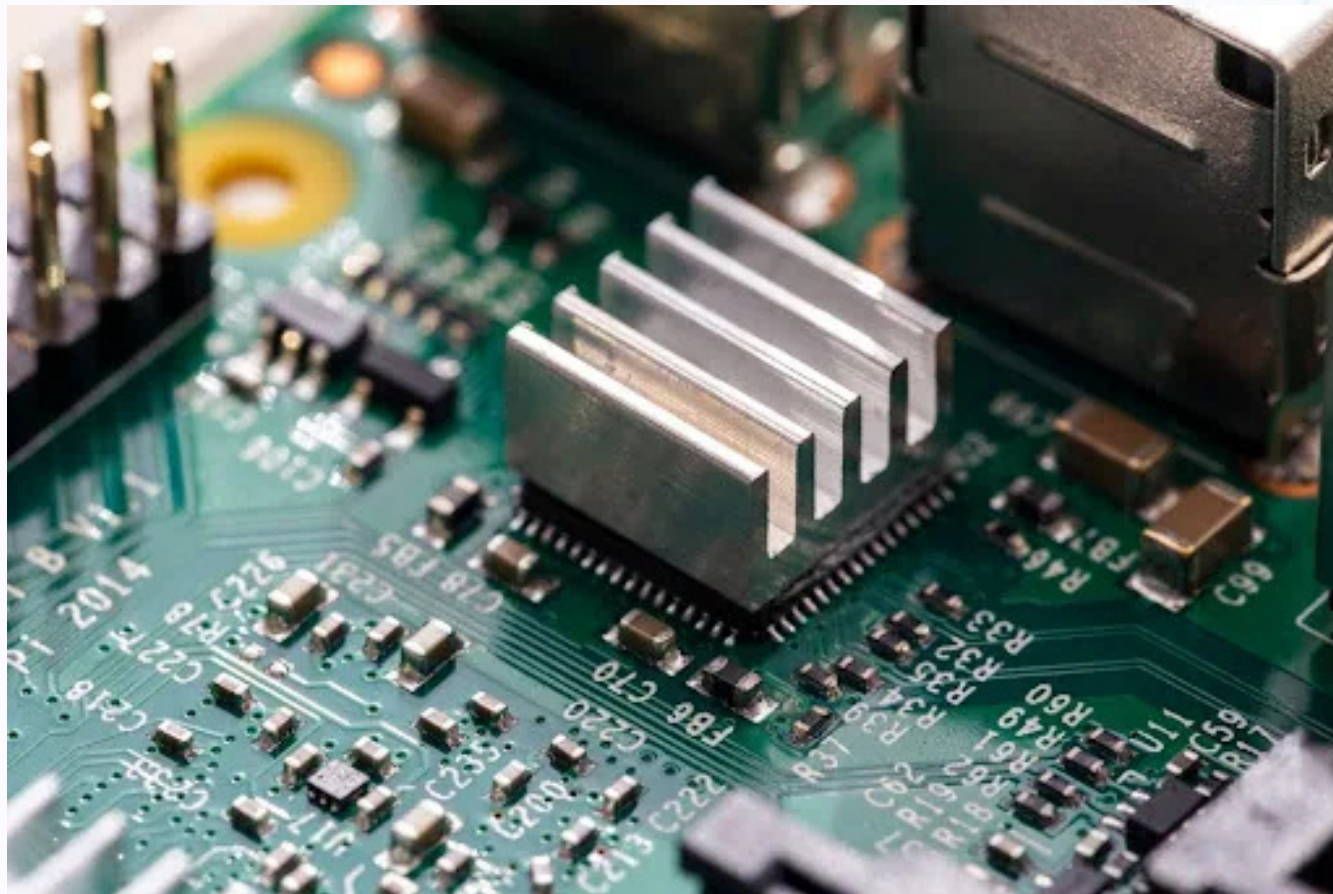
2. Copper Pours AND PLANES



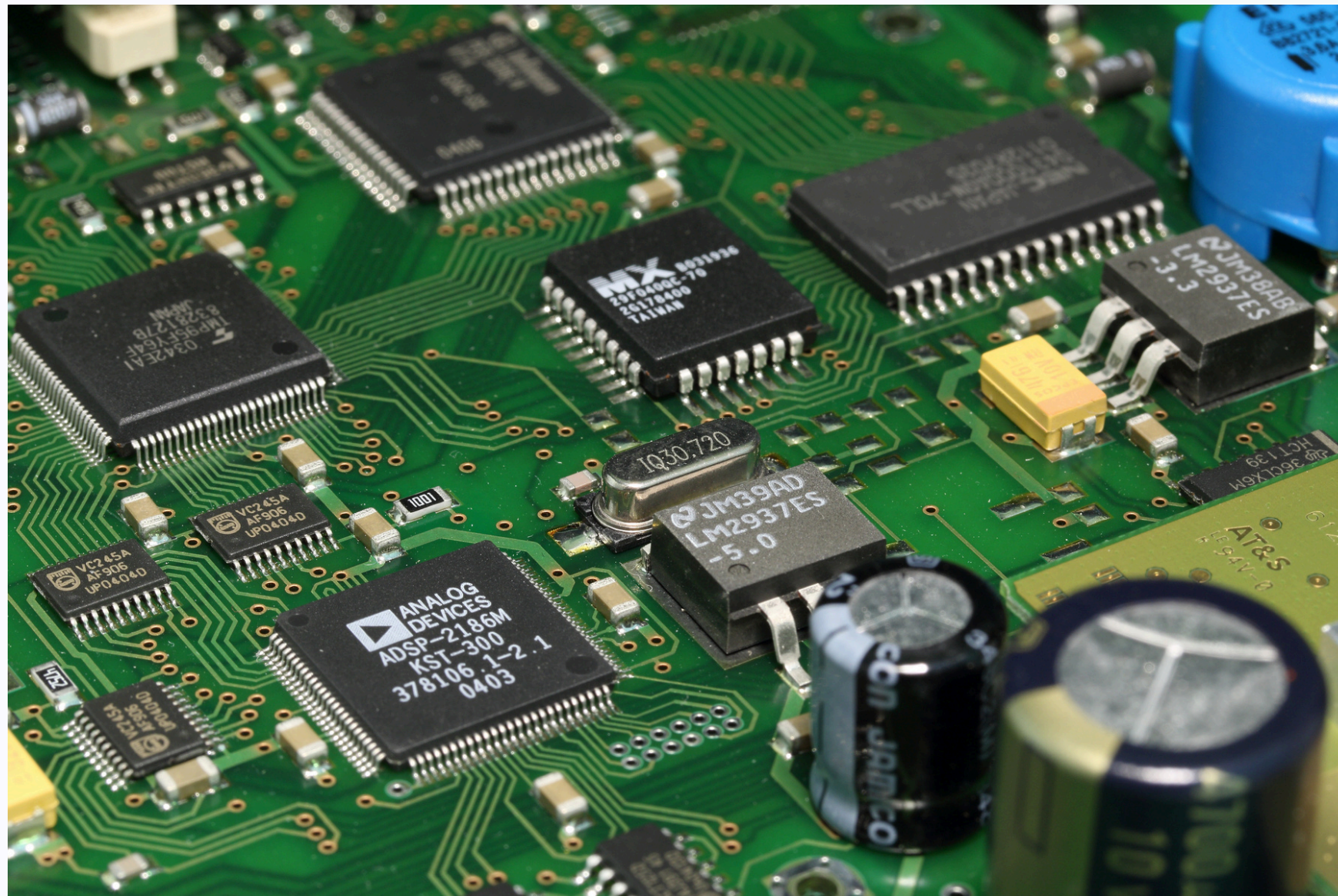
- Spreads heat across the PCB.
- Thicker copper improves heat conduction.
- Acts as a natural heat sink.

3. HEAT SINKS AND THERMAL INTERFACE MATERIALS

- Heat sinks draw heat away from components.
- Thermal pads/paste fill gaps to improve conduction.



4. Optimal Component Placement



- Spread high-power components to avoid hotspots.
- Place heat-sensitive components away from heat sources.
- Position heat-generating components near edges for better ventilation.

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Thank You

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What is EMI (ELECTROMAGNETIC INTERFERENCE)?

Unwanted electromagnetic energy that disrupts the performance of electronic devices.

Types of EMI:

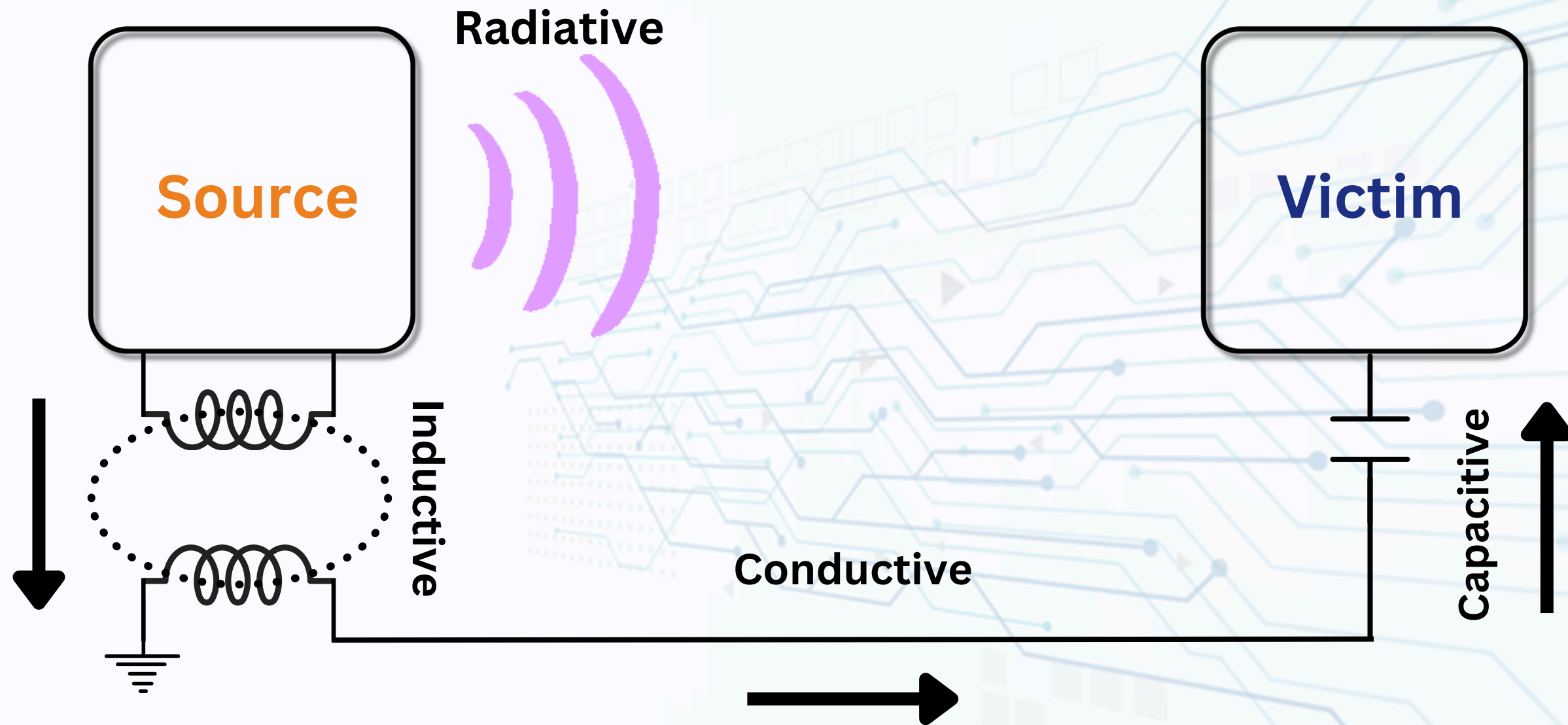
RADIATED EMI:

Energy emitted into the air (e.g., antennas, high-speed traces).

CONDUCTED EMI:

Interference transmitted through power or signal lines.

EMI & EMC



WHAT is EMC (ELECTROMAGNETIC COMPATIBILITY)?

- The ability of a device to function without causing or being affected by EMI.
- Ensures devices can coexist in the same environment without interference.

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SOURCES of EMI in PCBs

- High-speed circuits and switching components.
- Poor grounding and PCB layout.
- Inductive and capacitive coupling between traces.

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STRATEGIES TO REDUCE EMI IN PCBs

Use Ground Planes:

- Solid ground planes minimize loop areas and reduce noise.

Shielding:

- Enclose sensitive circuits with metal shields.

Trace Routing:

- Keep high-speed and noisy traces short.
- Avoid crossing signals over splits in the ground plane.

Decoupling Capacitors:

- Place capacitors near power pins of ICs to reduce noise.

Differential Signaling:

- Use paired signals to reduce radiated emissions.

STRATEGIES TO ACHIEVE EMC

Component Placement:

- Separate noisy components (e.g., oscillators, power circuits) from sensitive analog components.

PCB Stack-Up:

- Use multi-layer PCBs with dedicated ground and power planes for better isolation.

Isolation and Filtering:

- Use optocouplers or isolation transformers for signal isolation.

EMI Testing:

- Test and tweak designs in compliance with regulatory standards.

BEST PRACTICES FOR EMI/EMC in PCB DESIGN

- Minimize loop areas for return currents.
- Use controlled impedance for high-speed signals.
- Maintain consistent grounding and avoid floating grounds.
- Use thermal relief pads for grounding noisy components.

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