## UNIT II

## Schematic Sheet





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## Own Library

CREATION





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🗧 Control Panel - C:\L	Jsers\praga\Or	neDri	ve\Documents\	eagle\Test <mark>project 1 - EA</mark>
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File > New > Library

### GLE 7.5.0 Light

in this project.



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			Packag	ge			
			2		~		
	Device				~		

Select > Package



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# Select the wire > 21 tPlace > (x,y) coordination





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8.26 - 6.60 = 1.66 1.66 / 2 = 0.83

2.54 \* 3 = 7.62 10.16 - 7.62 = 2.54 2.54 / 2 = 1.27







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Set the copper pad Coordination







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1.27 + 2.54 = 3.81 3.81 + 2.54 = 6.35 6.35 + 2.54 = 8.89

Α	В	-10	A	$\langle \rangle$
1.27	-0.83	R	1.27	7.
3.81	-0.83	A.	3.81	7.
6.35	-0.83		6.35	7.
8.89	-0.83		8.89	7.







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Place all the copper pad at the right coordination

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## **Select > Symbol**

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File Edit Draw View Library Options Window Help



### **Select > Add > add the Symbol**

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## **Select > file to pin connection**







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After mapping all the pin, Verified symbol was obtained









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🗐 1 Schematic - C:\Users\prag	a\OneDrive\Dr_um	vent \cagle\	Test project 1\untitle
File Edit Draw View Tool	Library Options	Window	Help
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	Open		
● ● Sheets ■ × 2.54 mm (-	Update		
	Update all		
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## Click > Library > Use



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## Select > Library file > Open





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Name	Description			
> lem	LEM Current Senor/Transducer	P	\$1 OFFS	ET NC P\$5
> linear	Linear Devices	P <u>3</u>	\$2 INVER	TING V+ P\$6
> linear-techn	Linear Technology Devices	P <u>3</u>	B NON_	INV OUT P\$7
N lines		P <u>3</u>	54 V-	NULL P\$8
✓ LM741_FP				
LM741				
		Pa	ckage:	LM741
> lprs	Low Power Radio Solutions Ltd			
> lsi-computer	LSI Computer Systems, Inc.			
> lumiled	PHILIPS Lumiled Please read the des			
> maxim	Maxim Components			
> maxstream	MaxSream XBee Products			
> melexis	MELEXIS			
> memory	Generic Memories		tributa	Value
> memory-hit	Hitachi Memories		lindute	value
> memory-idt	IDT Memories			
	ads 🔽 Smds 🔽 Description 🔽 Preview			
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Select > Component > OK





# Our own library was imported to the schematic sheet





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## Schematic Tools



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AB

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## **COMPONENT IMPORT**



Import all the components and make all the necessary connections

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## ERC - Electrical Rule Check

🗐 1 Schematic - D:\Enthu teo	ch\PCB\TEST PROJECT 1\regulator.sch - EAG
<u>File Edit Draw Vie Too</u>	ls brary <u>O</u> ptions <u>W</u> indow <u>H</u> elp
🕞 🕀 🖨 🕋 🛔 1/1	<u>ERC</u>
	Error <u>s</u>
Sheets a × 2.54	Renumber parts
	Search
×, × U 1	Snap on grid
<i>□ ۶</i>	Statistics
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33 0	



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## ERC - Electrical Rule Check

Туре		
Consisten	y not checked (no board lo	oaded)
Errors (0)		
V Warnings	(1)	
• -		
A Par	t FRAME1 has no value	
Approved	t FRAME1 has no value (0)	
Approved	t FRAME1 has no value (0)	
Par Approved	t FRAME1 has no value (0)	
Par Approved	t FRAME1 has no value (0) Processed	









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